

2151

Docket No. 95-507

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

ERIMLI

Serial No.: 10/046,784

Filed: January 16, 2002

Group Art Unit: 2151

Examiner: Not assigned

For: ARRANGEMENT IN AN INFINIBAND CHANNEL ADAPTER FOR SHARING  
MEMORY SPACE FOR WORK QUEUE ENTRIES USING MULTIPLY-LINKED  
LISTS

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a Form PTO-1449 (PTO/SB/O8B) listing the enclosed document. It is respectfully requested that the Examiner consider the enclosed document in accordance with MPEP 609 by placing the Examiner's initials in the left column of the form PTO-1449, and returning the completed form PTO-1449 to the undersigned upon the next communication from the Patent Office.

This Information Disclosure Statement is filed after three months of filing the application, but before the mailing of a first action on the merits. Should a first action on the merits have been issued on the same day or before this Information Disclosure Statement is filed, please accept this Information Disclosure Statement under Rule 97(c) and charge the requisite Rule 17(p) fee to our Deposit Account No. 50-1130 under Order No. 95-507 for which purposes this paper is submitted in duplicate.

10/046,784

ERIMLI

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This Information Disclosure Statement is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

Respectfully submitted,

Manelli Denison & Selter PLLC

A handwritten signature in black ink, appearing to read 'L R Turkevich', written over the firm name.

Leon R. Turkevich

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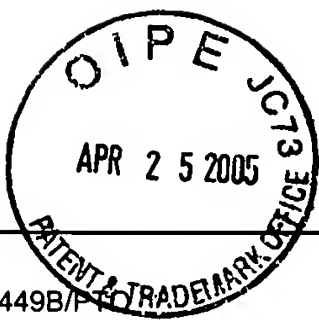
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**Date: April 25, 2005**



PTO/SB/08B (Modified)

Substitute for form 1449B/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT***(use as many sheets as necessary)***Complete if Known**

Application Number	10/046,784
Filing Date	January 16, 2002
First Named Inventor	ERIMLI
Group Art Unit	2151
Examiner Name	Not assigned

Sheet	1	of	1	Attorney Docket Number	95-507
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**OTHER PRIOR ART — NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		CASSIDAY, "InfiniBand™ Architecture Tutorial" Hot Chips, August 2000, Sun Microsystems	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.